

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3350	((((FPGA) or field adj2 programmable adj2 gate adj2 array) near9 hardware)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	AND	ON	2004/11/22 13:00
L2	1469	((((F adj2 P adj2 G adj2 A) or field adj2 programmable adj2 gate adj2 array) near9 hardware)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	AND	ON	2004/11/22 12:59
L3	1469	((field adj2 programmable adj2 gate adj2 array) near9 hardware)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	AND	ON	2004/11/22 13:00
L4	2738	((FPGA) near9 hardware)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	AND	ON	2004/11/22 13:00
L5	1467	((field adj1 programmable adj1 gate adj1 array) near9 hardware)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	AND	ON	2004/11/22 13:01
L6	109	L5 and (re-programmable or reprogrammable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	AND	ON	2004/11/22 13:01
L7	54	L6 and (print\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	AND	ON	2004/11/22 13:59
L8	0	L7 and (binary adj2 correlator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	AND	ON	2004/11/22 13:59

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((binary near9 correlator near9 hardware near9 (single adj5 bit)) same (print\$4 near9 press))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	AND	ON	2004/11/22 09:48
L2	0	((binary near9 correlator near9 (single adj5 bit)) same (print\$4 near9 press))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	AND	ON	2004/11/22 09:48
L3	1	(correlator near9 hardware near9 FPGA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	AND	ON	2004/11/22 09:51